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PATENT & TRADEMARK OFFICE

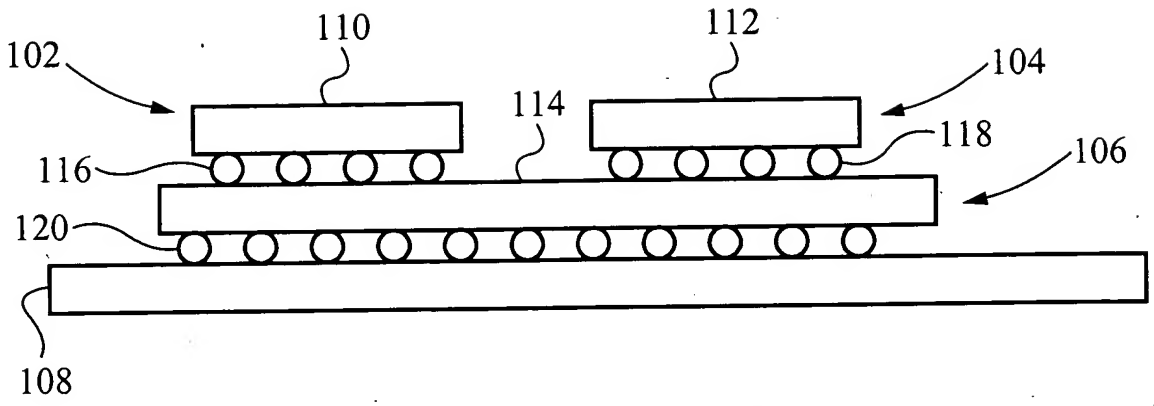


Fig.1 (PRIOR ART)

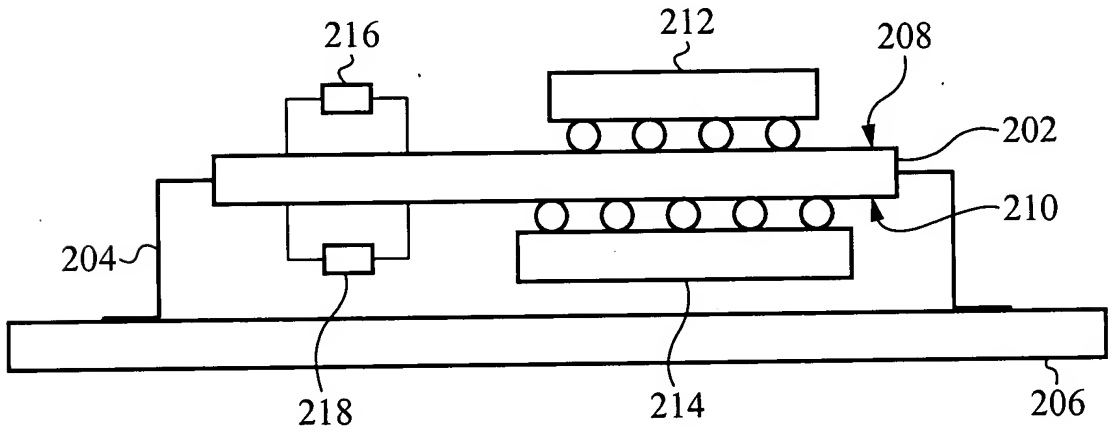


Fig.2A

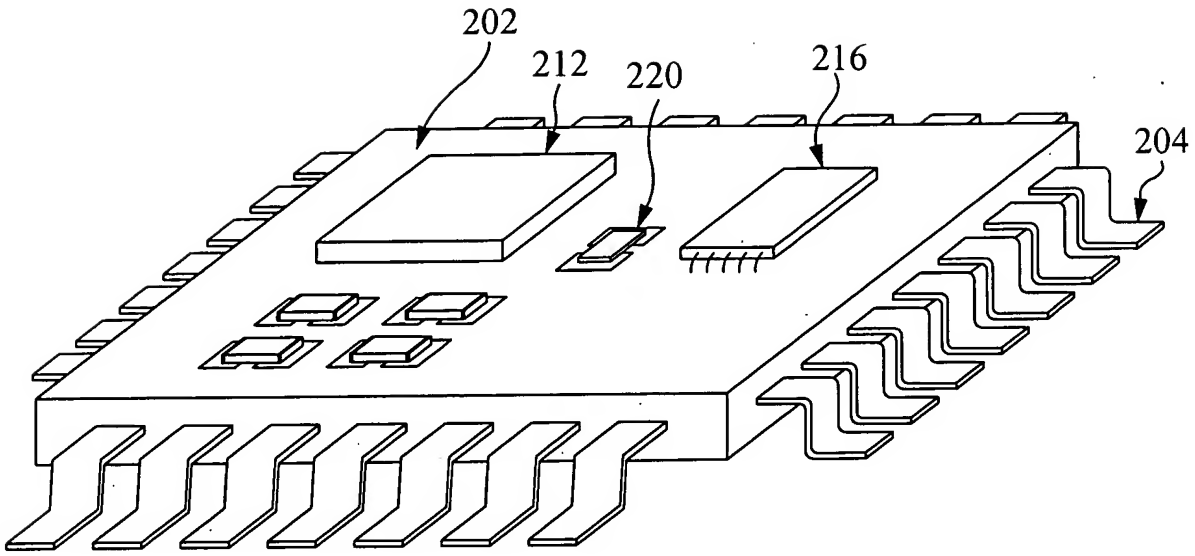
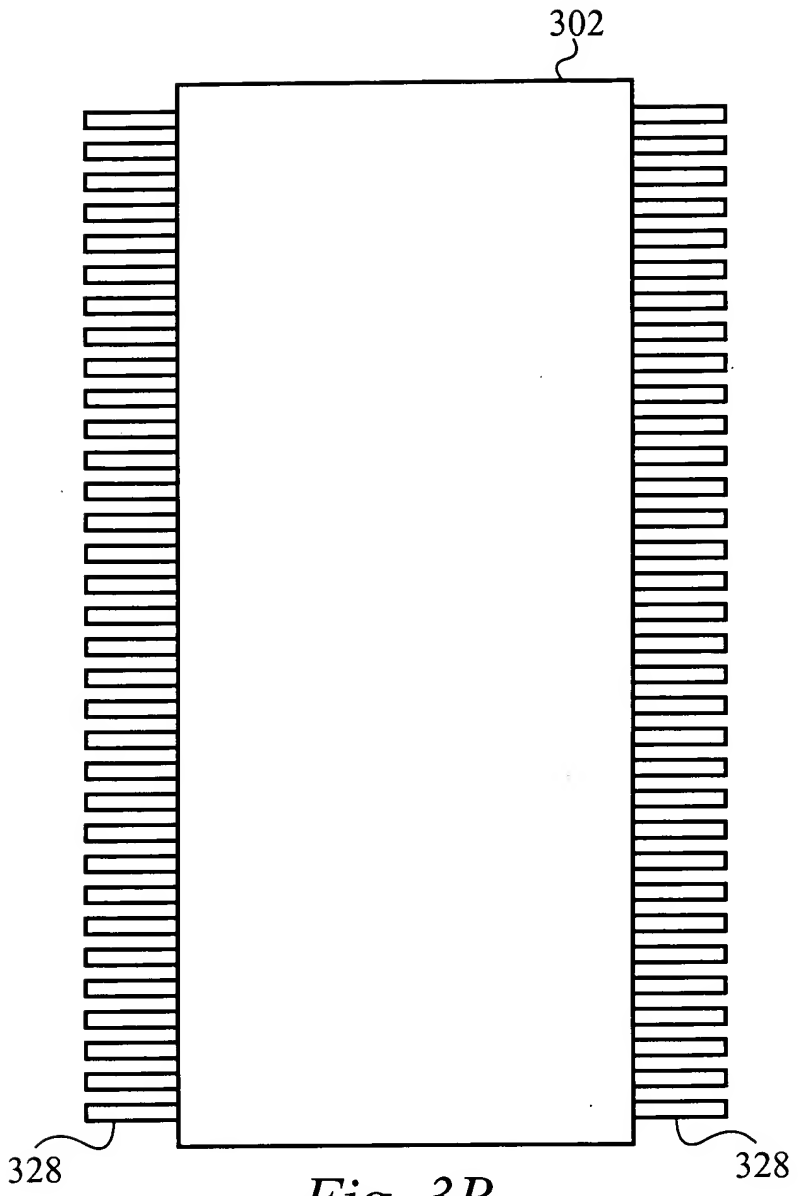


Fig.2B


302
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328

Fig. 3B

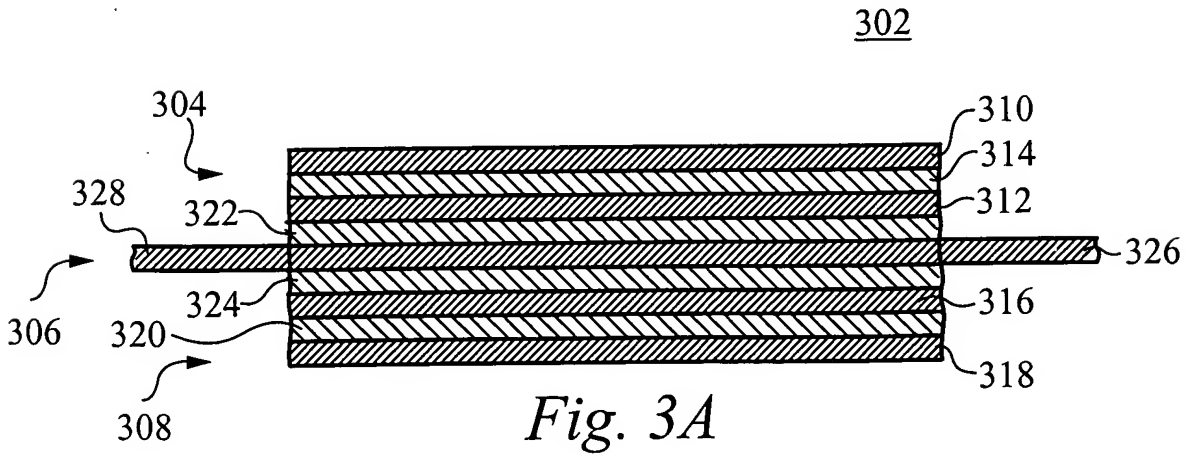
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Fig. 3A

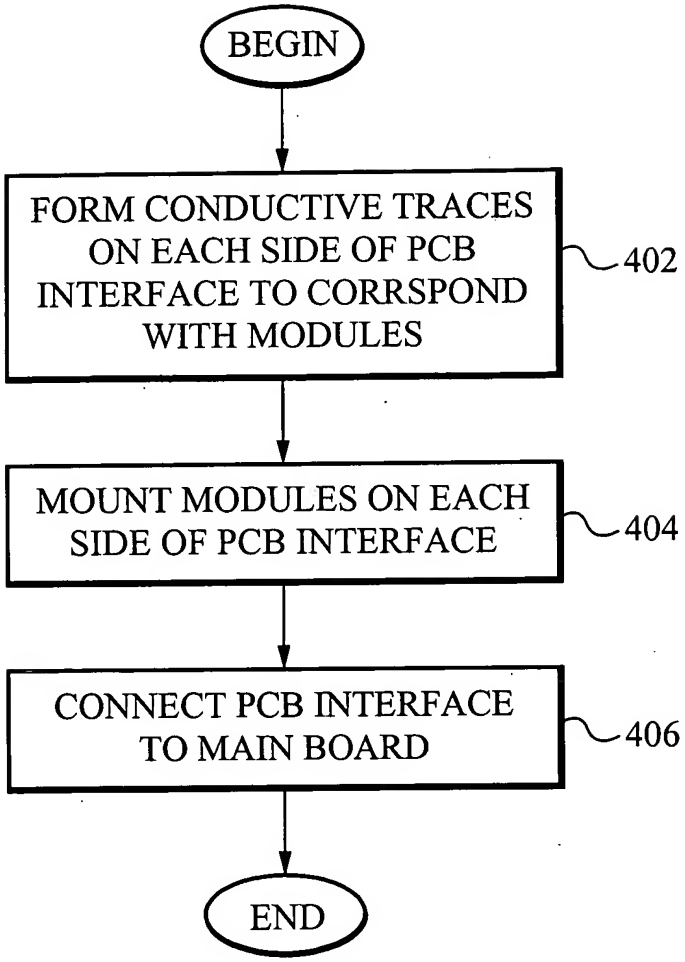
BEGIN
FORM CONDUCTIVE TRACES ON EACH SIDE OF PCB INTERFACE TO CORRSPOND WITH MODULES
402
MOUNT MODULES ON EACH SIDE OF PCB INTERFACE
404
CONNECT PCB INTERFACE TO MAIN BOARD
406
END

Fig. 4